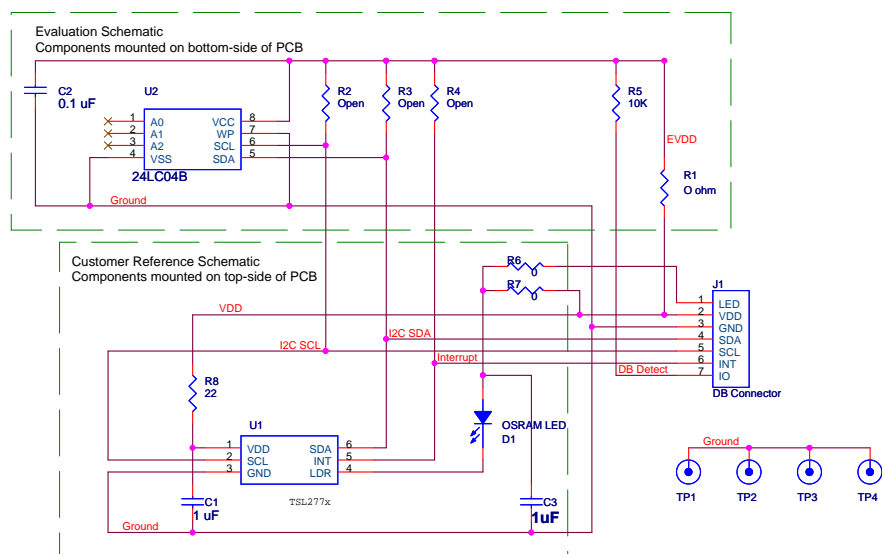



Notes:

- 4 layer PCB: 2 layers for Customer Reference and 2 layers for Evaluation.
- 20 mil trace (minimum) on all power and ground routes.
- Decoupling capacitors are physically close to U1 VDD and U2 VCC, respectively.
- Capacitor C3 only required when driving LED from 5V0,
- Designed for TSL277x, and can be used with TCS377x.
- All passive components are EIA0603 except R2 and C1 which are EIA0402.
- Assembly drawing:
- PCB drawing:



		
<b>Title</b> TSL277x Daughterboard		
<b>Size</b> B	<b>Document Number</b> TE-20-00108	<b>Rev</b> C
<b>Date:</b> Tuesday, April 05, 2011		
<b>Sheet</b> 1 of 1		